

source, and a current sink. The load connects to the load output; and the current source, and the current sink connect to a single diagnostic output and provide a current at the diagnostic output. The logic element can control the power switch and/or the current source and the current sink. The circuit configuration also includes a display element having light-emitting semiconductor diodes, a first terminal, and a second terminal. The first terminal electrically connects to the single diagnostic output of the semiconductor element. The second terminal electrically connected to the load output of the power switch. The light-emitting semiconductor diodes connects in antiparallel between the first terminal and the second terminal and outputs different visual information items when receiving different currents.


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In the Claims:

Claim 1 (Amended). A circuit configuration, comprising:


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an integrated power semiconductor element having a single diagnostic output, a logic element, a power switch for switching a load and having a load output, a current source, and a current sink; the load connected to said load output, the current source and the current sink being connected to said single diagnostic output and providing a current at said diagnostic output; said logic element controlling at least one of said power switch, and the current source and the current sink; and



a display element having light-emitting semiconductor diodes, a first terminal, and a second terminal; said first terminal electrically connected to said single diagnostic output of said integrated power semiconductor element; said second terminal electrically connected to said load output of said power switch; and said light-emitting semiconductor diodes being connected in antiparallel between said first terminal and said second terminal and outputting different visual information items when receiving different currents.


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Claim 4 (Amended). The circuit configuration according to claim 2, wherein said integrated power semiconductor element connects to the load.

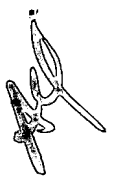
Claim 5 (Amended). The circuit configuration according to claim 1, wherein the current source and current sink are disposed in a half-bridge configuration relative to one another having a center tap, and the center tap is connected to said single diagnostic output.

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Claim 8 (Amended). The circuit configuration according to claim 1, wherein the current source and the current sink are MOSFETs.

Claim 9 (Amended). The circuit configuration according to claim 1, wherein said integrated power semiconductor element further includes an input terminal, an output terminal connected to the load, and a supply terminal for receiving a supply potential.



Claim 10 (Amended). The circuit configuration according to claim 9, wherein said integrated power semiconductor element is a low-side switch having a single supply terminal, at which a first supply potential is present, and outputting a second supply potential at said output terminal.

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Claim 14 (Amended). An integrated display element for diagnosing and displaying a disturbance in a semiconductor element, the integrated display element comprising:

an input terminal for connecting to the semiconductor element and for receiving a diagnostic signal from the semiconductor element;

a supply terminal for receiving a supply potential; and

light-emitting semiconductor diodes being connected in antiparallel between said input terminal and said supply terminal; said light-emitting semiconductor diodes outputting differing light signals as a function of a disturbance of the semiconductor element upon receiving the diagnostic signal fed through said input terminal.

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